

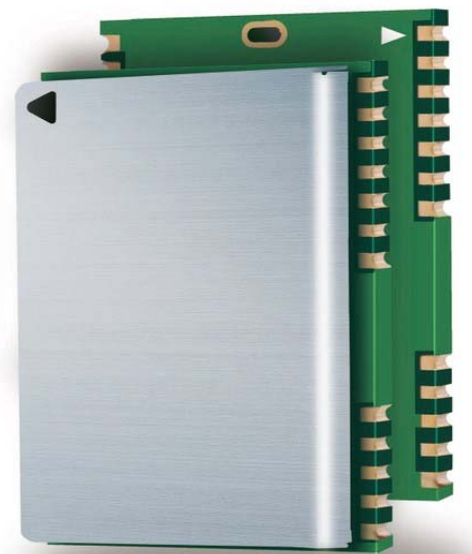


L20

Quectel GPS Engine

AGPS Application Note

L20_AGPS_AN_V1.0



Document Title	L20 AGPS Application Note
Revision	1.0
Date	2011-4-8
Status	Release
Document Control ID	L20_AGPS_AN_V1.0

General Notes

Quectel offers this information as a service to its customers, to support application and engineering efforts that use the products designed by Quectel. The information provided is based upon requirements specifically provided for customers of Quectel. Quectel has not undertaken any independent search for additional information, relevant to any information that may be in the customer's possession. Furthermore, system validation of this product designed by Quectel within a larger electronic system remains the responsibility of the customer or the customer's system integrator. All specifications supplied herein are subject to change.

Copyright

This document contains proprietary technical information of Quectel Co., Ltd. Copying of this document, distribution to others, and communication of the contents thereof, are forbidden without permission. Offenders are liable to the payment of damages. All rights are reserved in the event of a patent grant or registration of a utility model or design. All specification supplied herein are subject to change without notice at any time.

Copyright © Quectel Wireless Solutions Co., Ltd. 2011

Contents

Contents	2
0. Revision History	3
1. What's CGEE?	4
1.1. Related Documents	4
1.2. Abbreviations	4
2. How to Use CGEE?	5
2.1. Reference Design	5
2.2. Recommended EEPROM.....	6
3. How to Implement CGEE?	7

0. Revision History

Revision	Date	Author	Description of change
1.0	2011-4-8	Crystal HE/Ree ZHANG	Initial

1. What's CGEE?

The document describes how to use CGEE (Client Generated Extended Ephemeris) supported by SiRF starIV chipset. CGEE is the one type of the AGPS. Shorter time for cold/warm start, particularly warm start only about 10 seconds and faster positioning can be achieved by use of the CGEE. The stored ephemeris can be available within the next 3 days.

Note:

This AGPS application note is directed towards L20 R1.0 firmware version.

1.1. Related Documents

Table 1: Related documents

SN	Document name	Remark
[1]	L20_EVB_UGD	L20 EVB User Guide
[2]	L20_GPS_Protocol	L20 GPS Protocol Specification
[3]	L20_HD_V1.0	L20 Hardware Document

1.2. Abbreviations

Table 2: Abbreviations

Abbreviation	Description
CGEE	Client Generated Extended Ephemeris
GPS	Global Positioning System
GGA	GPS Fix Data
GLL	Geographic Position – Latitude/Longitude
GSA	GNSS DOP and Active Satellites
GSV	GNSS Satellites in View
NMEA	National Marine Electronics Association
OSP	One Socket Protocol
TTFF	Time-To-First-Fix
UART	Universal Asynchronous Receiver & Transmitter
VDOP	Vertical Dilution of Precision

2. How to Use CGEE?

2.1. Reference Design

The functional schematic diagram of CGEE is shown in Figure 1.

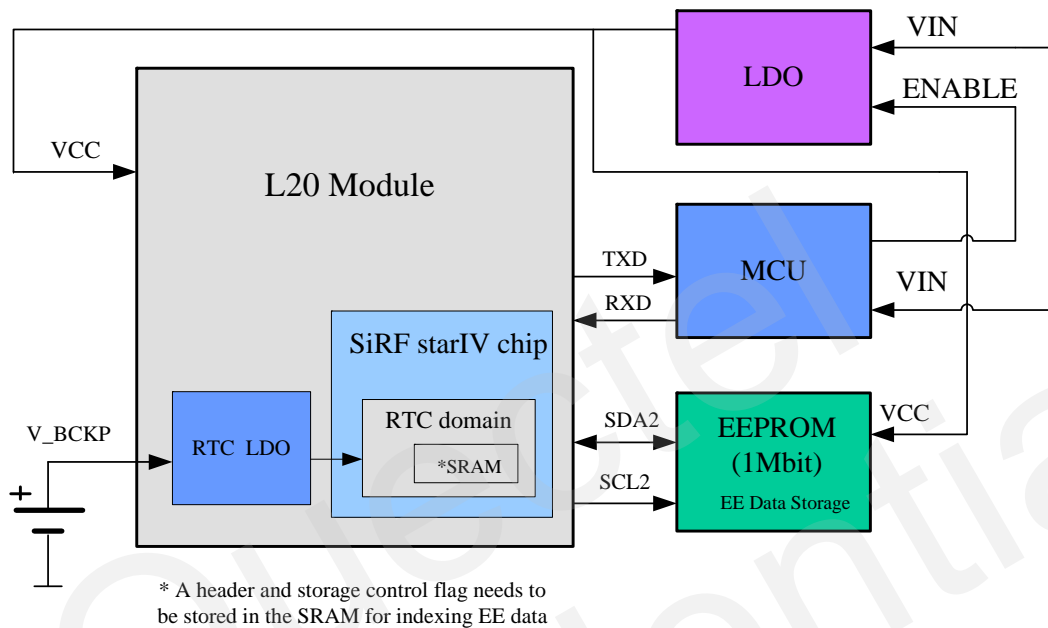


Figure 1: Functional Schematic Diagram of CGEE

The external 1Mbit EEPROM is used to store Client-Generated Extended Ephemeris (CGEE) data generated by SiRF starIV chip through I2C⁽¹⁾ port. Control flag and header of EE Data are written in the SRAM which is in the RTC domain in the chip. When the module is restarted, the information in the SRAM will function as the index of EEPROM. V_BCKP is used to supply power to the RTC domain. When V_BCKP is removed: the information saved in the SRAM will be lost and the CGEE data files stored in the serial EEPROM will not be accessed accordingly when L20 starts up next time. If L20 is powered on again, the system will generate new CGEE data files which will be stored in the eternal EEPROM and the corresponding new header and control flag will be written in the SRAM.

Thus, it is strongly recommended two separate voltage sources, VCC and V_BCKP, should be used in the design of the module. V_BCKP should be kept alive as long as possible.

(1) The I2C port is open drain output and supports up to 400kbps for accessing the EEPROM. The data line and clock line are internally pulled up to VCC by 2.2K resistors.

2.2. Recommended EEPROM

Some types of 1Mbit EEPROM have been tested to be available. The type and manufacturer are listed in table 3.

The reference circuit of EEPROM is shown in Figure 2.

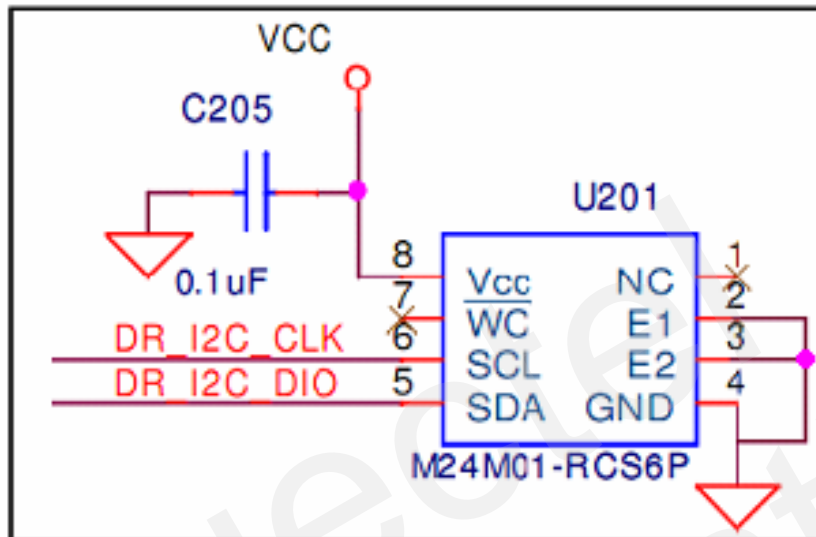


Figure 2: EEPROM Reference Circuit

Table 3: Recommended EEPROM

Manufacturer	Part Number
ST	M24M01
Seiko Instruments Inc.	S-24CM01C
Atmel	AT24C1024B

3. How to Implement CGEE?

The following introduces the guideline on how to operate CGEE when the L20 is powered on and how to restart CGEE when L20 is powered back.

Step 1: Switch NMEA protocol to OSP protocol mode

Click the menu: Receiver-Command-Switch Protocol. See Figure 3.

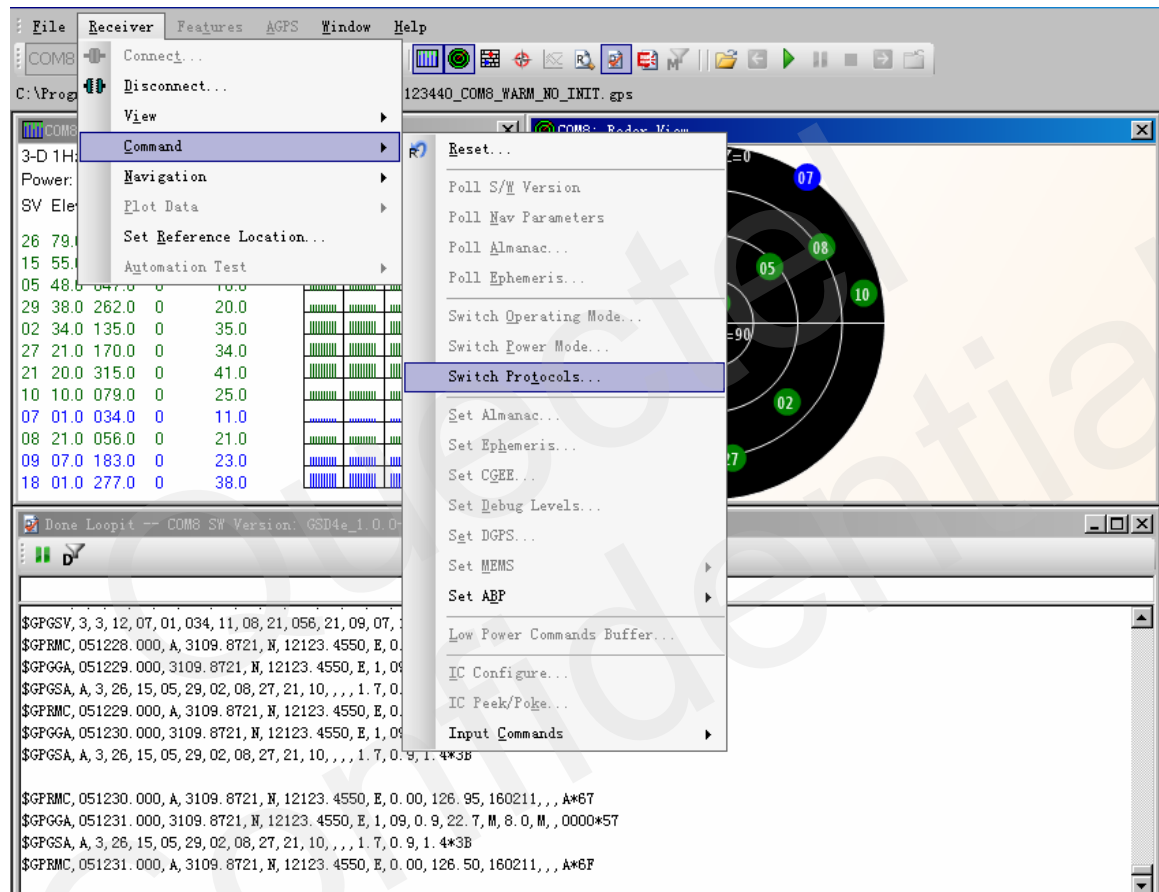


Figure 3

Then click Set, the module will switch to OSP protocol mode. See figure 4 on the Page 8.

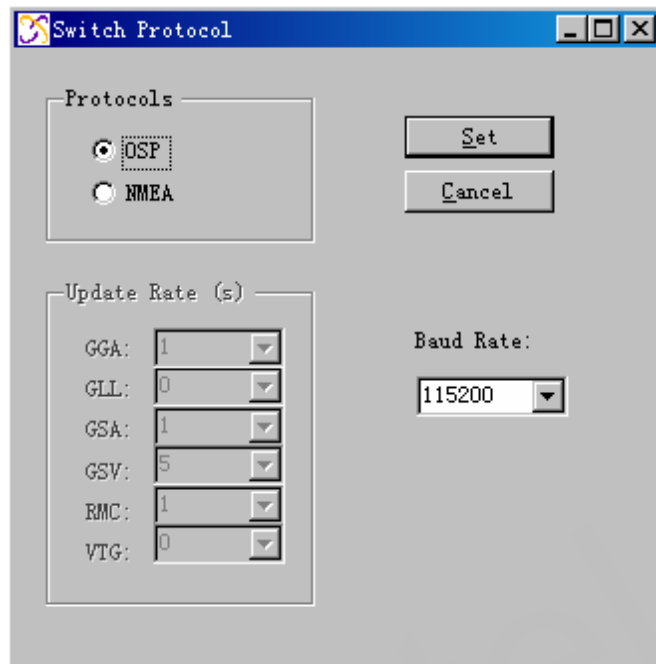


Figure 4

Step 2: Access EEPROM

Click the menu: Receiver-Command-Input Command-User Defined Message. See figure 5.

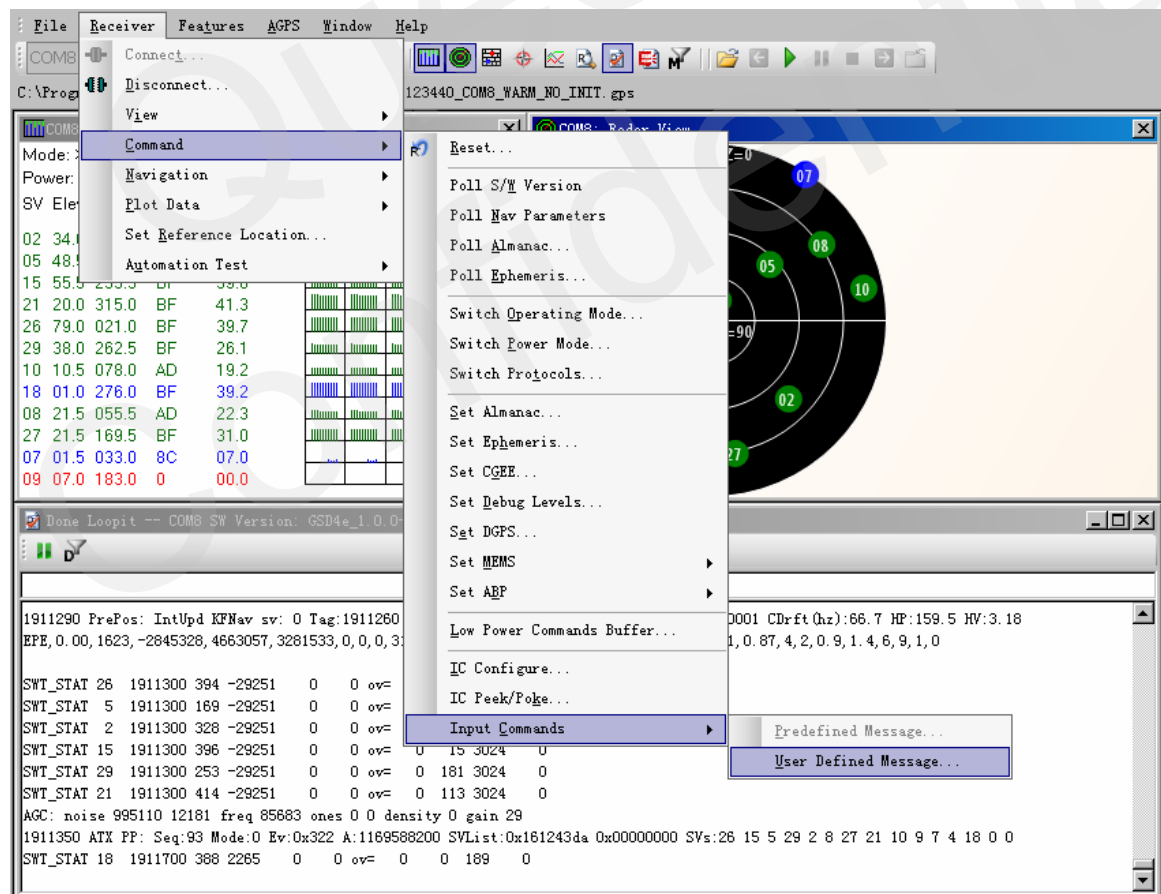


Figure 5

Input defined message, **A0 A2 00 03 E8 FD 01 01 E6 B0 B3**, which allows the module to access

the EEPROM. Then click the button “Send”, L20 will send the EE data generated locally to the EEPROM. See figure 6.

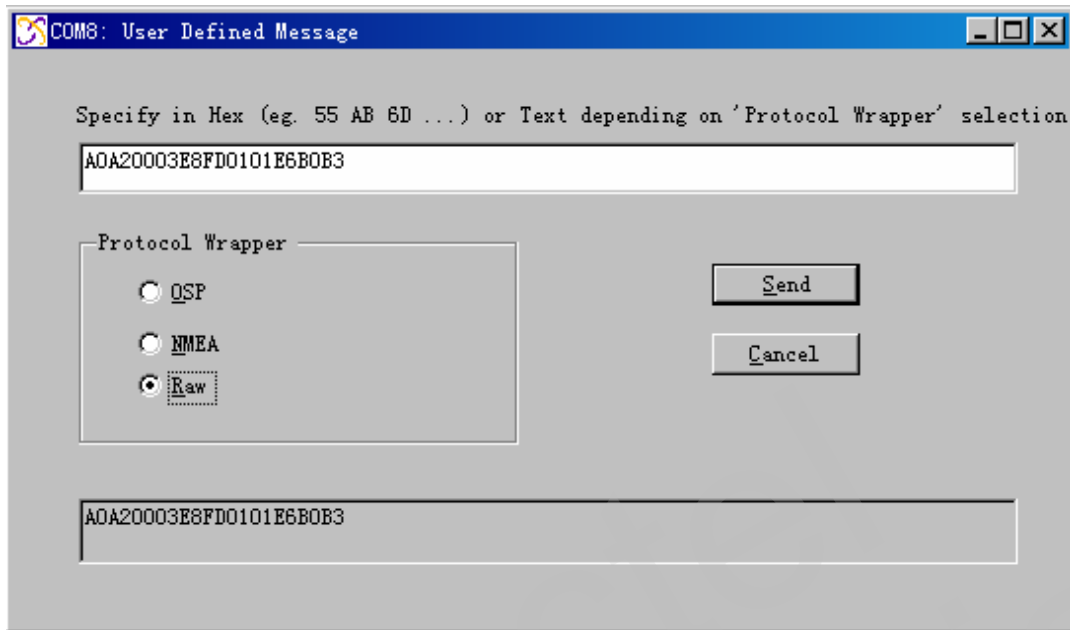


Figure 6

Note: If customers need to shut down the VCC power, Step 3 to 7 is required; otherwise, the following steps can be ignored.

Step 3: Acquire the index from EEPROM

Input defined message: **A0 A2 00 0A B2 03 02 04 80 04 25 28 02 5C 01 EA B0 B3.**

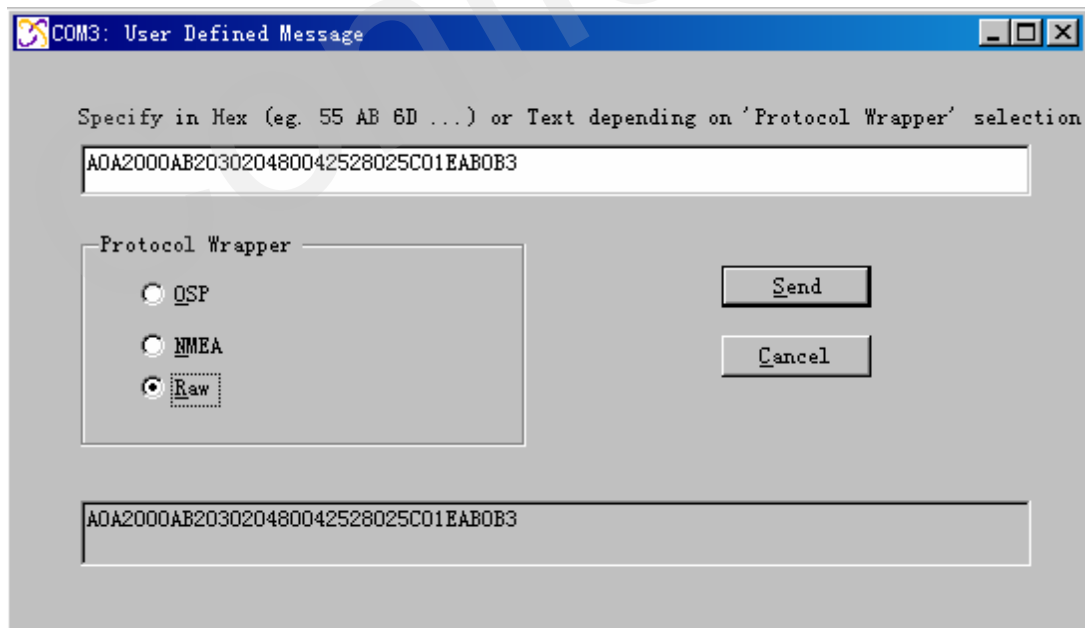


Figure 7

Annotations:

The checksum is transmitted with high order byte first and followed by the low byte. This is the so-called big-endian order.

High Byte	Low Byte
< 0x7F	Any value

The checksum is 15-bit checksum of the bytes in the payload data. The following pseudo code defines the algorithm used.

Enable message to be the array of bytes which will be sent by the transport.

Set msgLen to the number of bytes in the message array to be transmitted.

Index = first

Checksum = 0

While index < msgLen

checkSum = checkSum + message[index]

checksum= checksum AND (2¹⁵-1)

checkSum = checkSum AND (2¹⁵-1)

For more detailed information, please refer to document [2]

QUECTEL



Shanghai Quectel Wireless Solutions Co., Ltd.

Room 501, Building 13, No.99, TianZhou Road, Shanghai, China 200233

Tel: +86 21 54453668

Mail: info@quectel.com